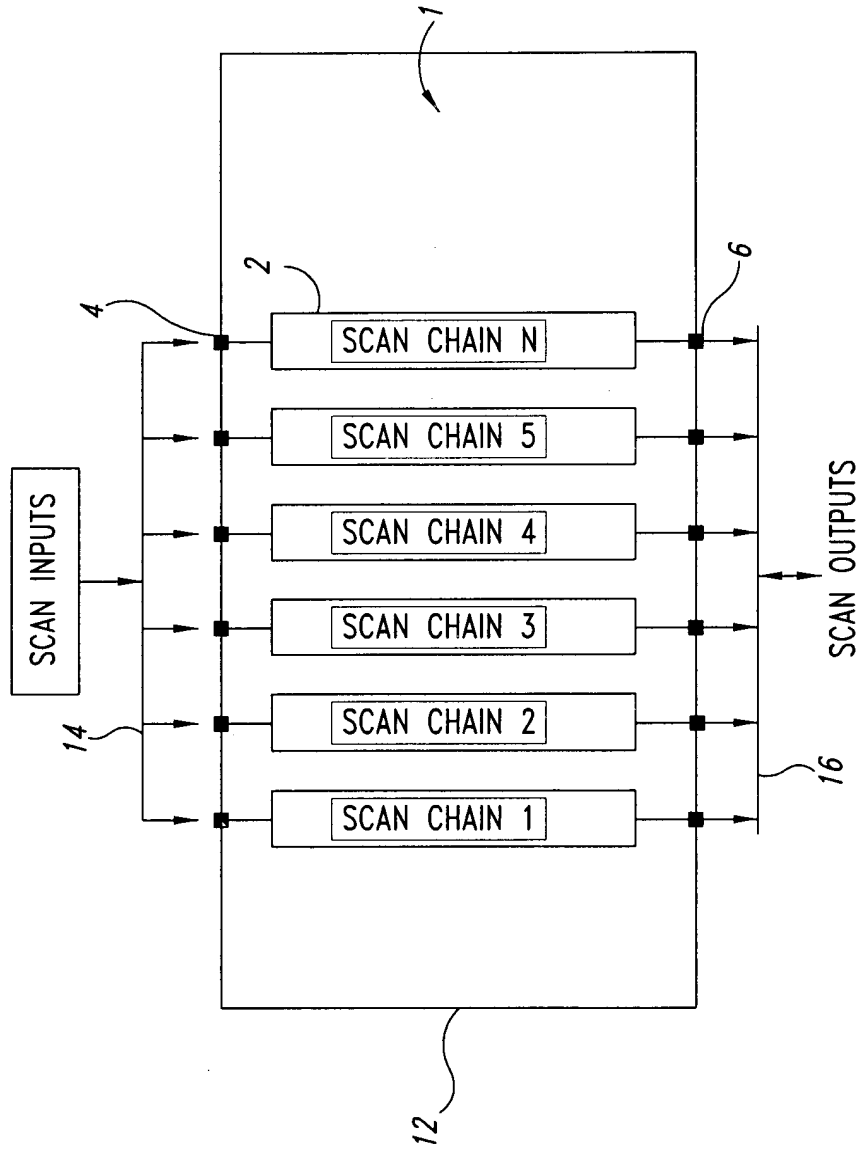


*Fig. 1*  
 (Prior Art)



*Fig. 2*  
*(Prior Art)*

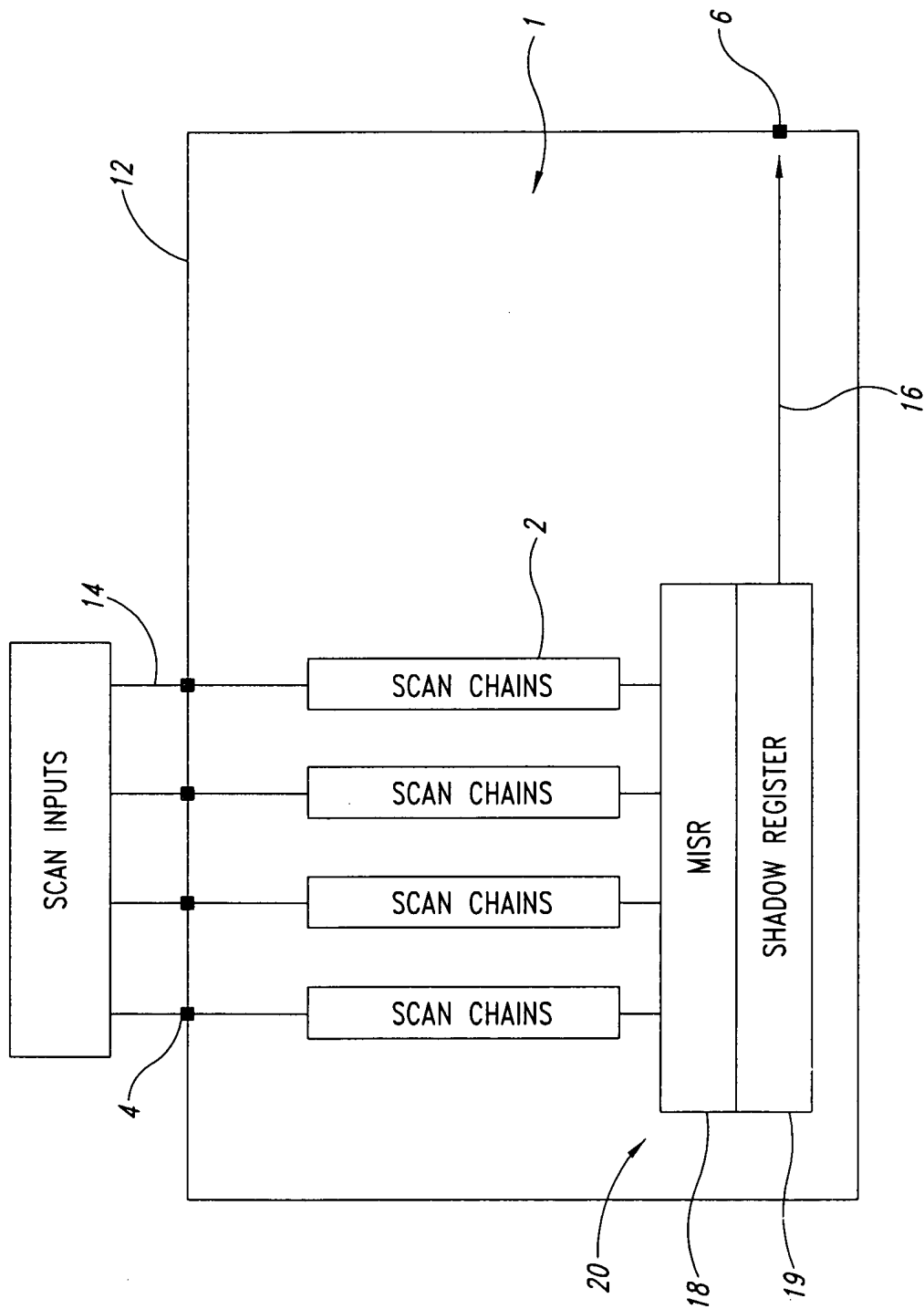


Fig. 3

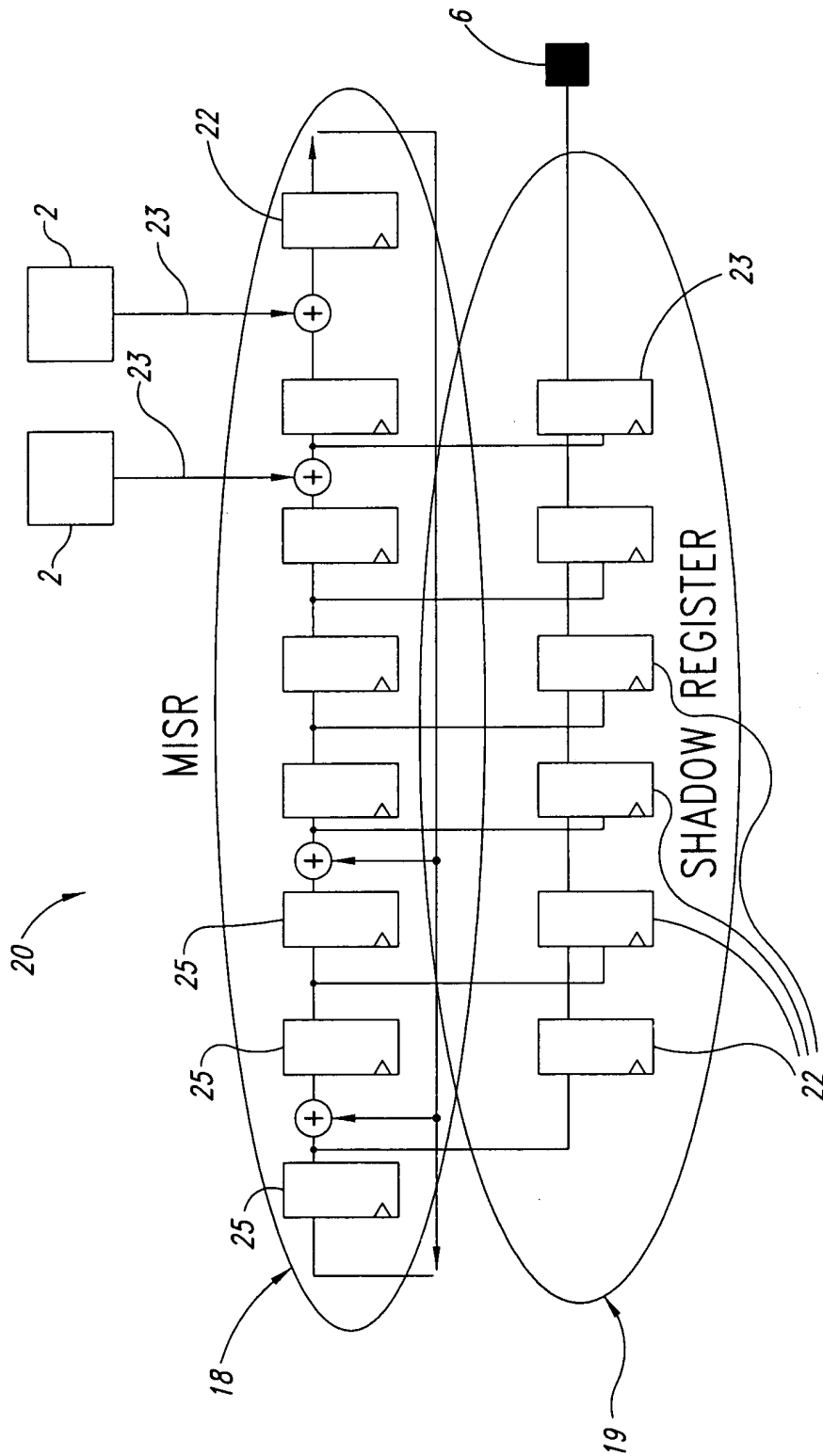


Fig. 4

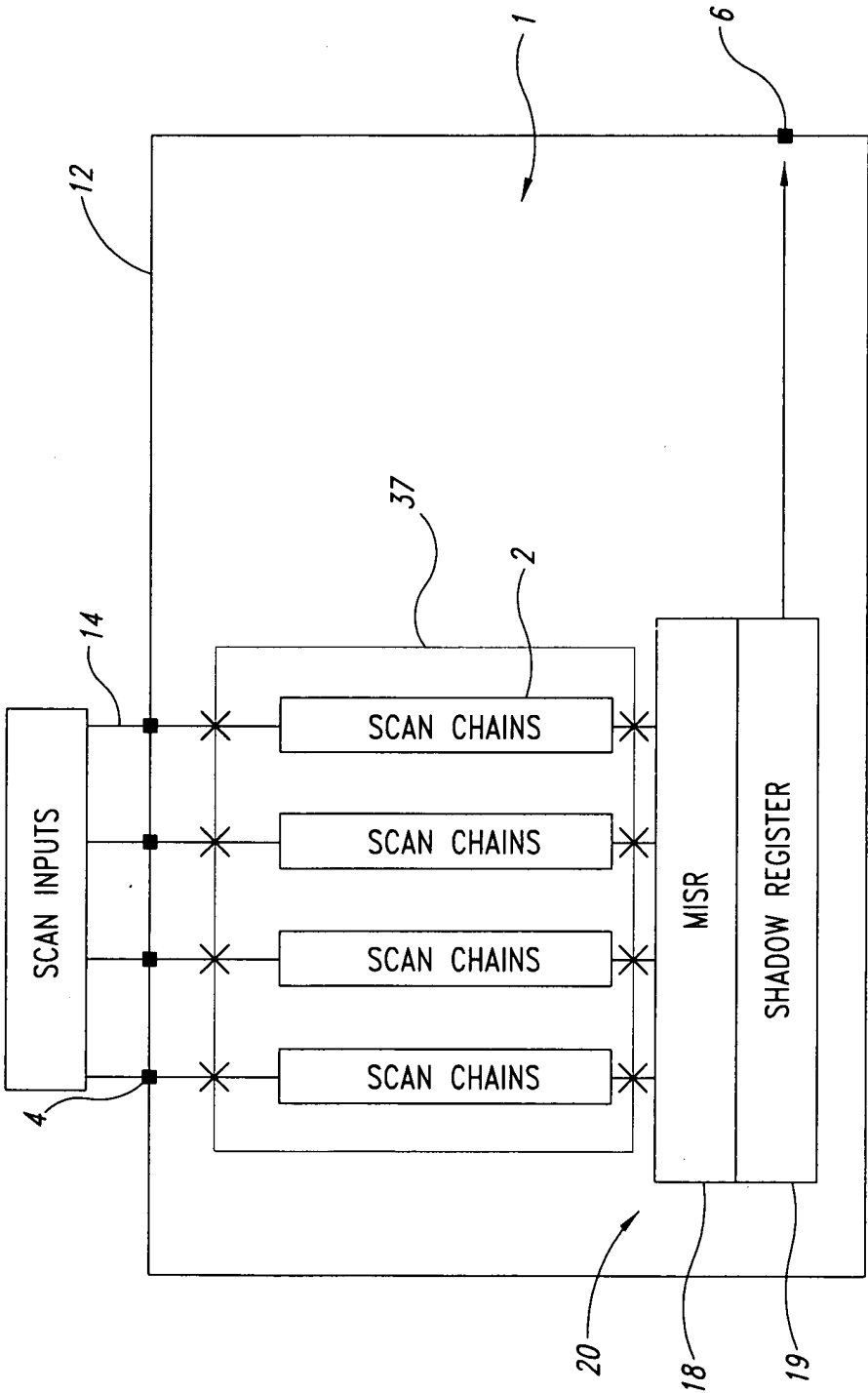


Fig. 5

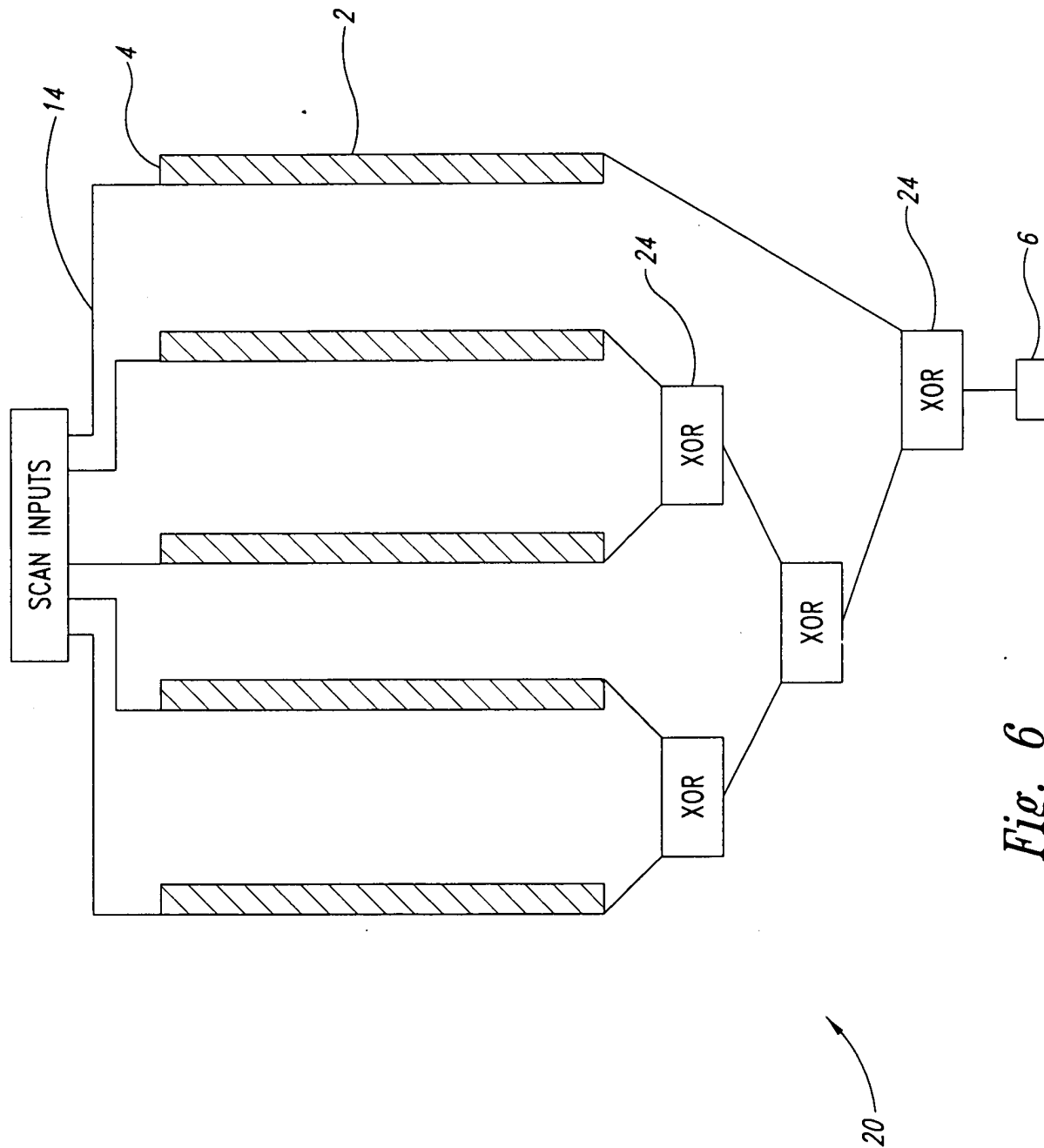


Fig. 6

Figure 7 is a schematic diagram of a circuit scan output arrangement. The diagram shows a circuit 20 connected to a MISR (Multiple Input Signature Register) 19. The circuit 20 includes a first input 4, a second input 5, and a third input 14. The first input 4 is connected to a first input 31 of a first block 2a. The second input 5 is connected to a first input 30 of a second block 2b. The third input 14 is connected to a first input 33 of a third block 2c. The outputs of blocks 2a, 2b, and 2c are connected to the MISR 19. The MISR 19 is connected to a first output 26 and a second output 27. The first output 26 is connected to a first input 28 of a first block 2d. The second output 27 is connected to a first input 29 of a second block 2e. The outputs of blocks 2d and 2e are connected to the MISR 19.

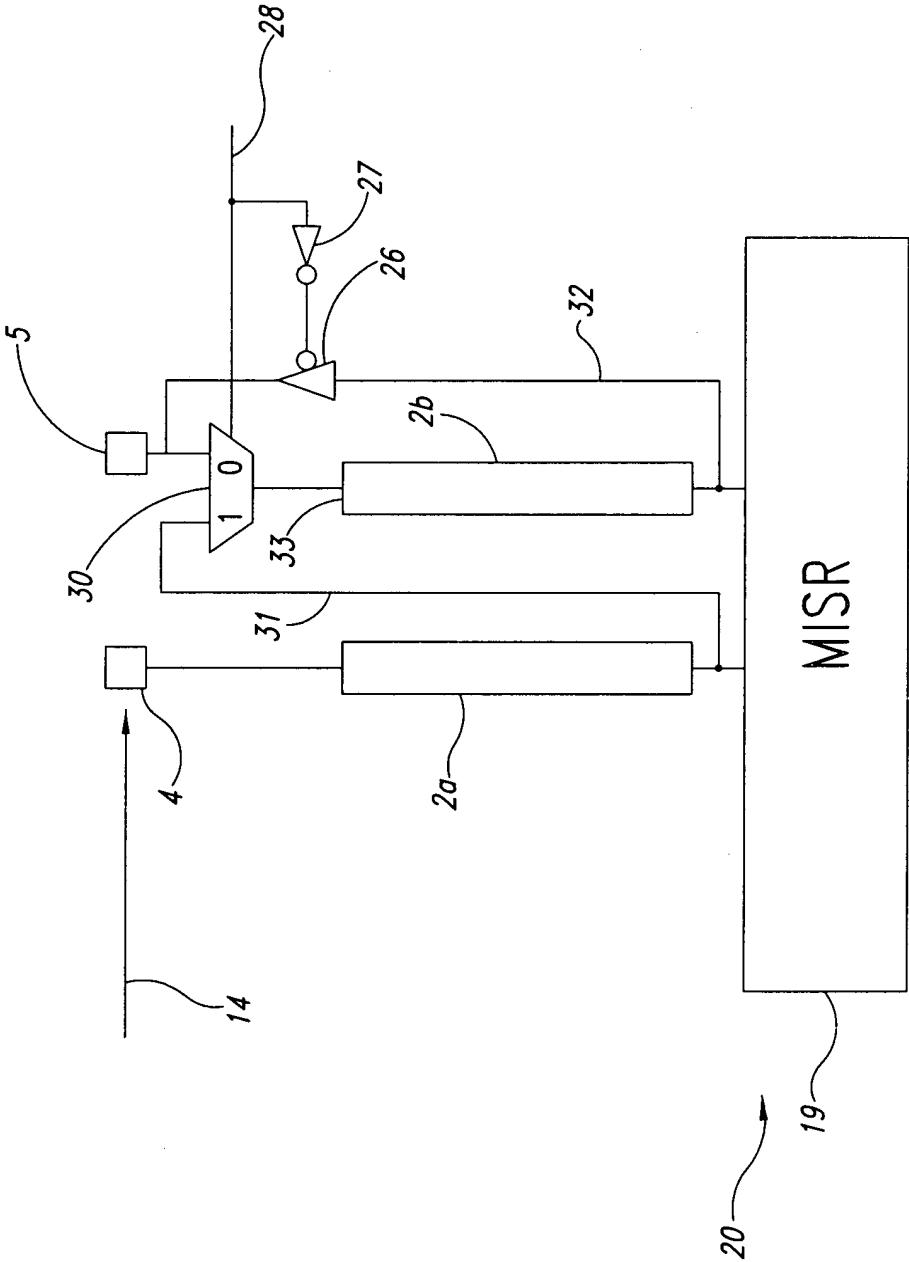


Fig. 7